

328555(28)

**B. E. (Fifth Semester) Examination,
April-May 2019
(New Scheme)
(ET & T Engg. Branch)**

ADVANCED MICROPROCESSOR and INTERFACING

Time Allowed : Three hours

Maximum Marks : 80

Minimum Pass Marks : 28

Note : Attempt all questions. Part (a) of each question is compulsory. Attempt any two part from (b), (c) and (d).

Unit-I

- 1. (a) Why Microprocessor 8086 faster than 8085? 2
- (b) Discuss the pipeline architecture of 8086 Micro processor. 7

328555(28)

PTO

- (c) Draw and discuss typical maximum mode of 8086 system. 7
- (d) Explain the architecture of I/O processor 8089. 7

Unit-II

- 2. (a) Briefly handle loop up tables in 8086. 2
- (b) Write an ALP to add two 8-bit ASCII number. 7
- (c) Explain the addressing modes of sequential control flow instruction. 7
- (d) Explain memory organization of 8086. 7

Unit-III

- 3. (a) Explain following signals : 2
 - (i) \overline{DTR}
 - (ii) \overline{RTS}
- (b) Explain in detail DMA controller. 7
- (c) Explain interfacing of 8257 with 8086. 7
- (d) Explain in detail the priority modes of 8259 PIC. 7

328555(28)

Unit-IV

4. (a) Define Keyboard. 2
- (b) Explain the interfacing of ADC 0808 with 8255. 7
- (c) Explain the interfacing between 8086 and LCD. 7
- (d) Explain features of 8279. 7

Unit-V

5. (a) Explain behaviour of prefetch queue in Jump and CALL instructions. 2
- (b) Discuss the difference between 8086 and 80386. 7
- (c) Explain paging and memory management unit of 80386. 7
- (d) Draw and explain the internal Architecture of 80386. 7

<http://www.csvtuonline.com>

Whatsapp @ 9300930012

Your old paper & get 10/-

पुराने पेपर्स भेजे और 10 रुपये पायें,

Paytm or Google Pay से